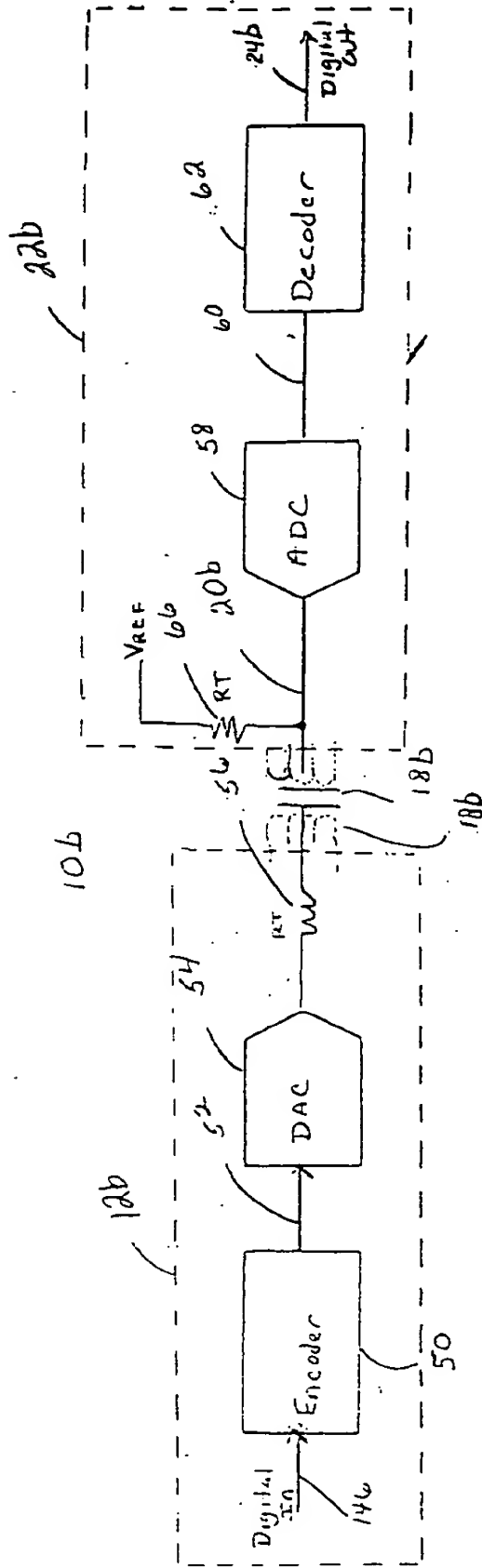


Fig. 4



# ENCODER

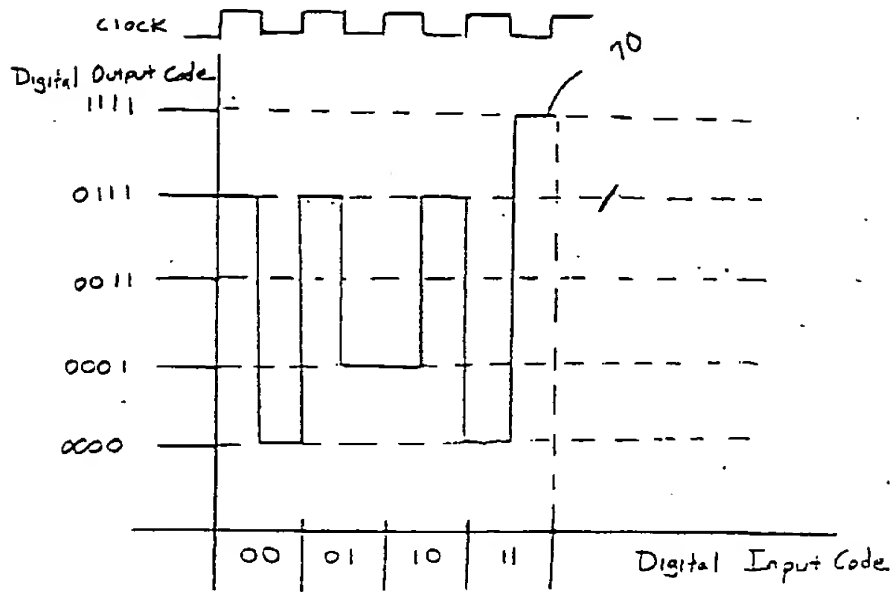


Fig. 6

DAC

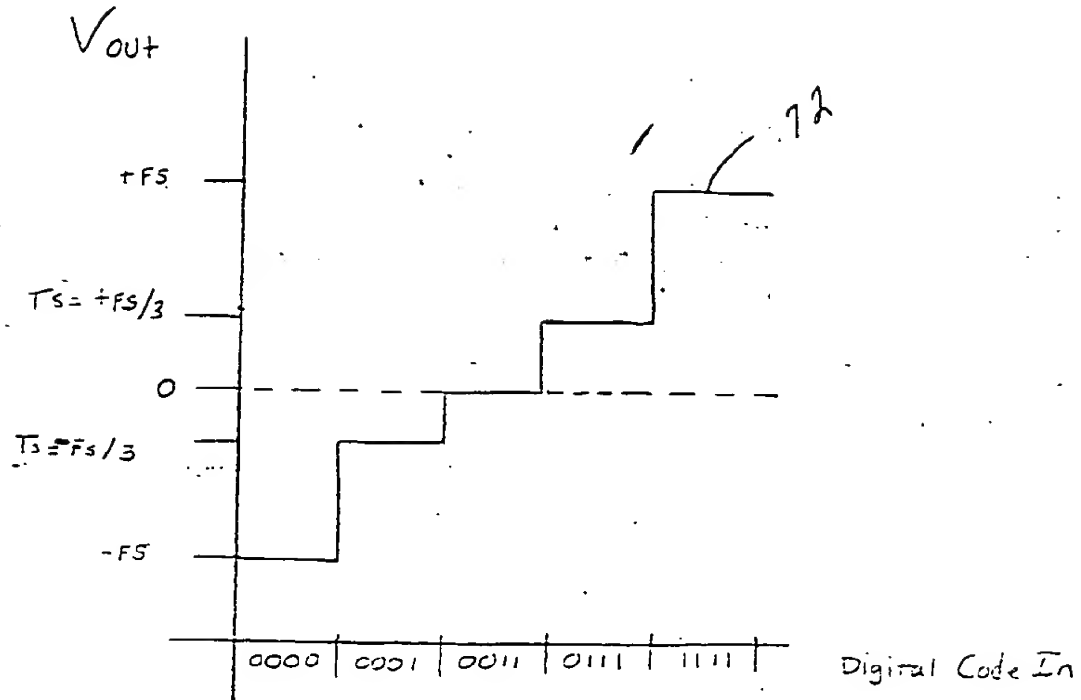
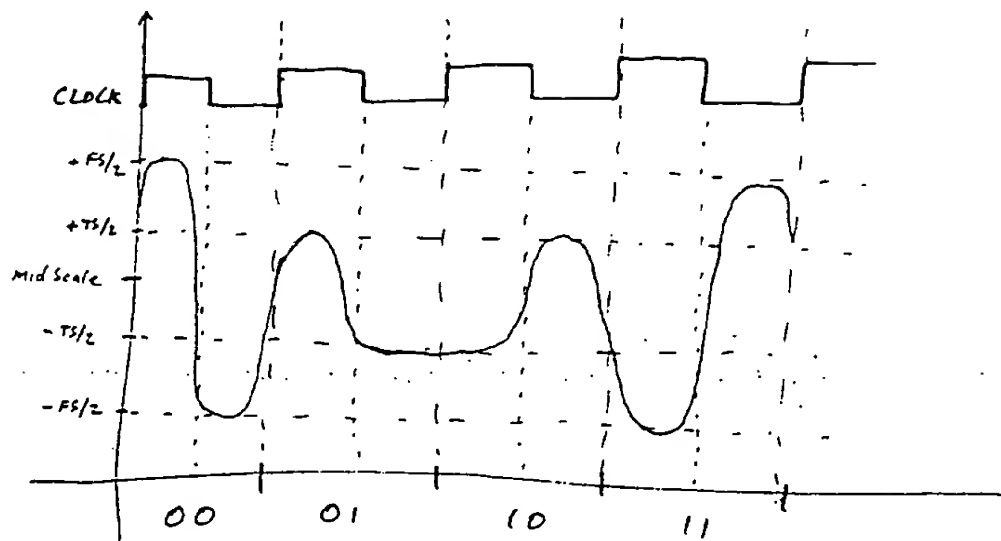


Fig. 7

Fig. 8A Barrier Pin Signal



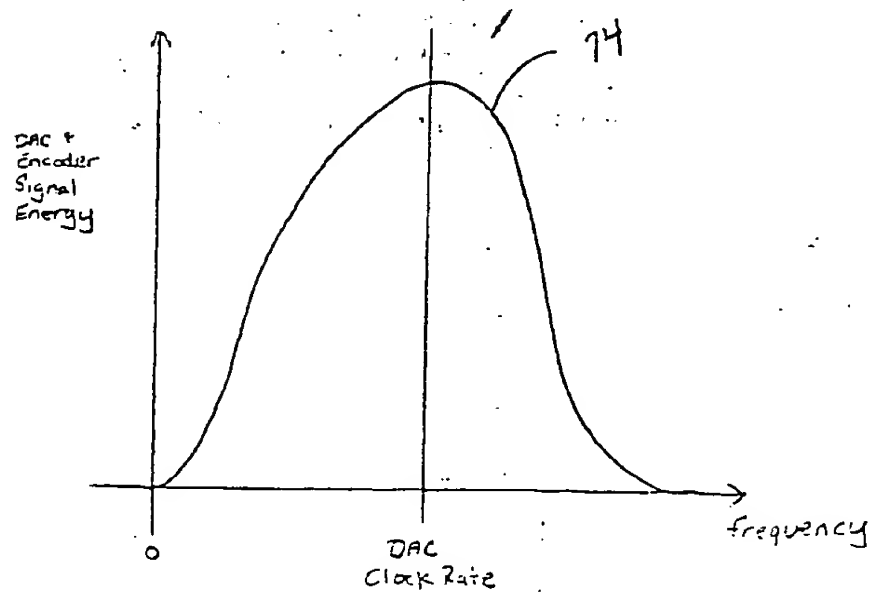


Fig. 8B

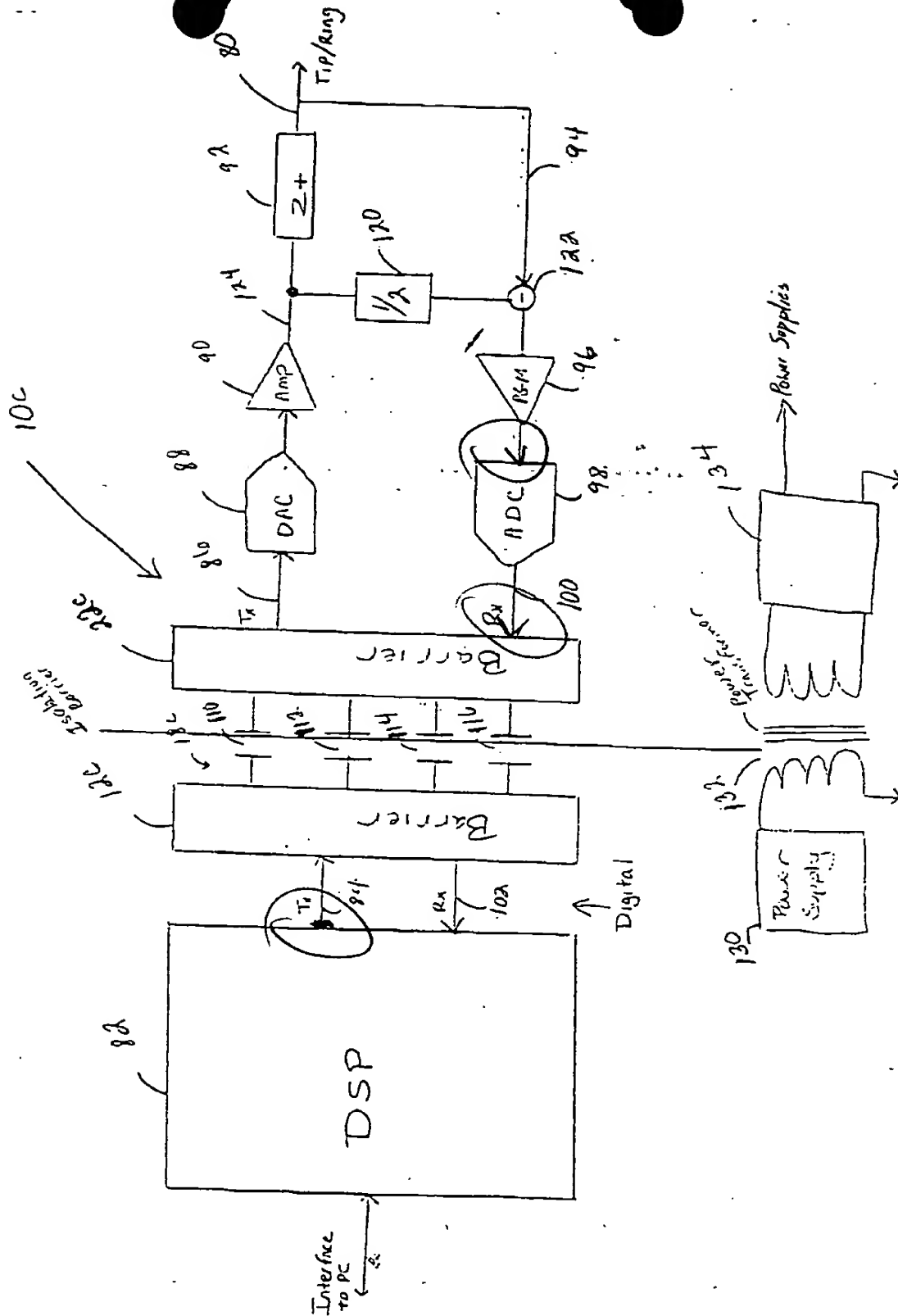


Fig. 9



Fig. 10



# Full Bridge System

10e

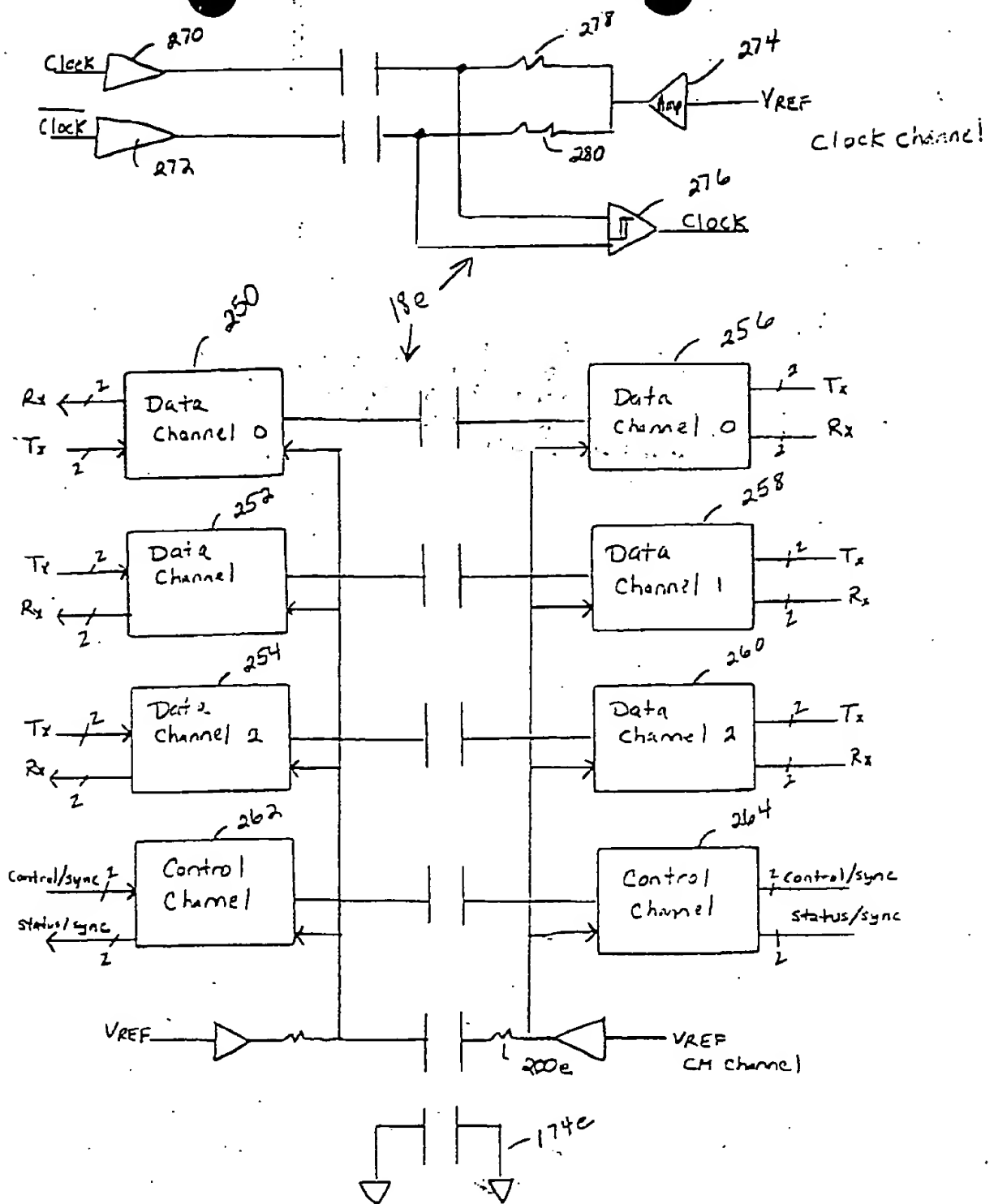


Fig. 11

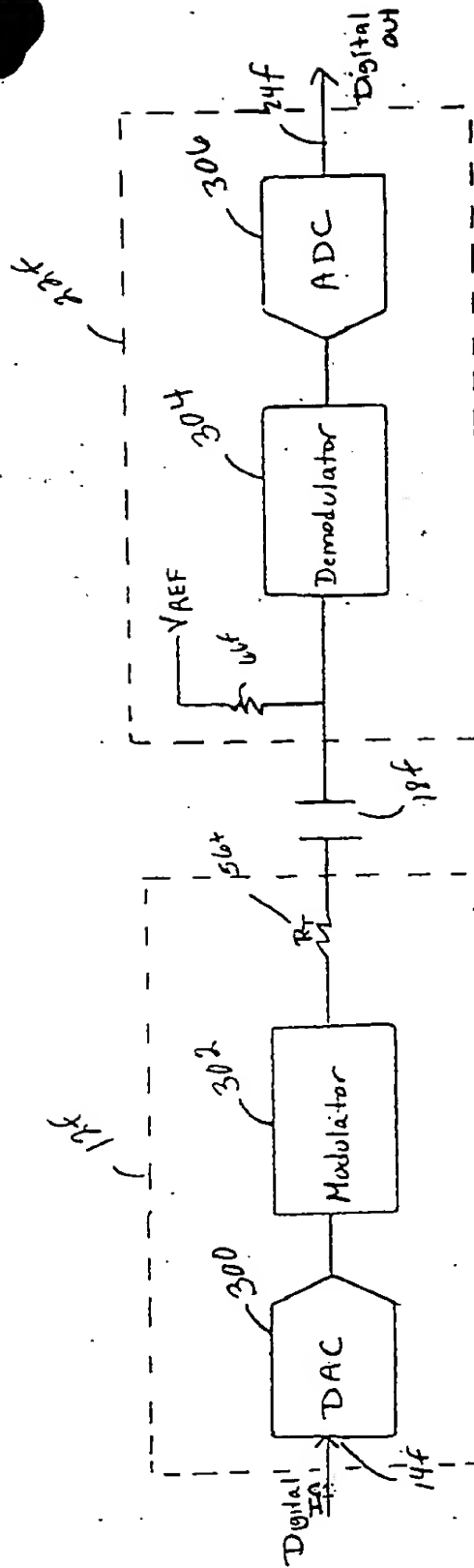


Fig. 12